CPC9909
High Efficiency, Off-Line, High Brightness LED Driver

Features
- 8V_{DC} to 550V_{DC} Input Voltage Range
- >90% Efficiency
- Stable Operation at >50% Duty Cycle
- Drives Multiple LEDs in Series/Parallel
- Regulated LED Current
- Linear or PWM Brightness Control Inputs
- Resistor-Programmable Minimum Off-Time
- SOIC-8 RoHS Compliant Package
- Buck or Boost Configuration

Applications
- Flat-Panel Display RGB Backlighting
- Signage and Decorative LED Lighting
- DC/DC or AC/DC LED Driver Applications

Description
The CPC9909 is a low cost, high-efficiency, offline, high-brightness (HB) LED driver manufactured using IXYS Integrated Circuits Division's high voltage BCDMOS on SOI process. This driver has an internal regulator that allows it to operate from 8V_{DC} to 550V_{DC}. This wide input operating voltage range enables the driver to be used in a broad range of HB LED applications.

The CPC9909 features pulse frequency modulation (PFM) with a constant peak-current control scheme. This regulation scheme is inherently stable, allowing the driver to be operated above 50% duty cycle without open loop instability or sub-harmonic oscillations. LED dimming can be implemented by applying a small DC voltage to the LD pin, or by applying a low frequency PWM signal to the PWMD pin.

The CPC9909 is available in a standard 8-lead SOIC package and a thermally enhanced 8-lead SOIC package with an exposed thermal pad (EP).

Ordering Information

<table>
<thead>
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<th>Description</th>
</tr>
</thead>
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<td>CPC9909N</td>
<td>SOIC-8 (100/Tube)</td>
</tr>
<tr>
<td>CPC9909NTR</td>
<td>SOIC-8 Tape &amp; Reel (2000/Reel)</td>
</tr>
<tr>
<td>CPC9909NE</td>
<td>SOIC-8 EP (Exposed Pad) (100/Tube)</td>
</tr>
<tr>
<td>CPC9909NETR</td>
<td>SOIC-8 EP (Exposed Pad) Tape &amp; Reel (2000/Reel)</td>
</tr>
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1.1 Package Pinout

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>V_IN</td>
<td>Input voltage</td>
</tr>
<tr>
<td>2</td>
<td>CS</td>
<td>LED Current Sense input. Internal current sense threshold is set at $V_{CS\text{ (high)}}$. The external sense resistor sets the maximum LED current.</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>Device Ground</td>
</tr>
<tr>
<td>4</td>
<td>GATE</td>
<td>External MOSFET gate driver output</td>
</tr>
<tr>
<td>5</td>
<td>PWMD</td>
<td>Low-frequency PWM dimming control input with internal pull-down resistor.</td>
</tr>
<tr>
<td>6</td>
<td>V_DD</td>
<td>Regulated supply voltage output. Requires a storage capacitor to GND. Can be overdriven by external voltage applied to $V_{DD}$.</td>
</tr>
<tr>
<td>7</td>
<td>LD</td>
<td>Linear Dimming. Apply a voltage less than $V_{CS\text{ (high)}}$ to dim the LED(s).</td>
</tr>
<tr>
<td>8</td>
<td>RT</td>
<td>Resistor to GND sets the minimum off-time.</td>
</tr>
<tr>
<td></td>
<td>EP</td>
<td>Electrical and thermal conductive pad on the bottom of CPC9909NE. Connect this pad to ground and provide sufficient thermal coupling to remove heat from the package.</td>
</tr>
</tbody>
</table>
1.3 Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Maximum</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage to GND</td>
<td>$V_{IN}$</td>
<td>-0.5 to +560 V</td>
<td>V</td>
</tr>
<tr>
<td>Inputs and Outputs Voltage to GND</td>
<td>$CS, LD, PWMD, GATE$</td>
<td>-0.3 to $V_{DD}+0.3$</td>
<td>V</td>
</tr>
<tr>
<td>$V_{DD}$, Externally Applied</td>
<td>$V_{DD,EXT}$</td>
<td>15</td>
<td>V</td>
</tr>
<tr>
<td>Power Dissipation:</td>
<td>$P_D$</td>
<td>2.5</td>
<td>W</td>
</tr>
<tr>
<td>SOIC-8 With Thermal Tab</td>
<td></td>
<td>0.975</td>
<td>W</td>
</tr>
<tr>
<td>SOIC-8 W/O Thermal Tab</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Junction Temperature, Operating</td>
<td>$T_J$</td>
<td>-55 to +150 °C</td>
<td>°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>$T_A$</td>
<td>-55 to +85 °C</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$T_{STG}$</td>
<td>-55 to +150 °C</td>
<td>°C</td>
</tr>
</tbody>
</table>

Electrical absolute maximum ratings are at 25°C.

Absolute maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.
1.4 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Unit</th>
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</thead>
<tbody>
<tr>
<td>Input Voltage Range</td>
<td>V_IN</td>
<td>8</td>
<td>-</td>
<td>550</td>
<td>V_DC</td>
</tr>
<tr>
<td>PWMD Frequency</td>
<td>f_PWMD</td>
<td>500</td>
<td>-</td>
<td>-</td>
<td>Hz</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>T_A</td>
<td>-40</td>
<td>-</td>
<td>+85</td>
<td>°C</td>
</tr>
</tbody>
</table>

1.5 Electrical Characteristics

Unless otherwise specified, all electrical specifications are provided for T_A=25°C.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Symbol</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Unit</th>
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<tbody>
<tr>
<td><strong>Input</strong></td>
<td>DC Input Voltage</td>
<td>V_IN</td>
<td>8</td>
<td>-</td>
<td>550</td>
<td>V_DC</td>
</tr>
<tr>
<td>Shut-Down Mode Supply Current</td>
<td>V_IN=8 to 550V, PWMD to GND</td>
<td>I_IN</td>
<td>-</td>
<td>0.3</td>
<td>0.6</td>
<td>mA</td>
</tr>
<tr>
<td><strong>Regulator</strong></td>
<td>Voltage Regulator Output</td>
<td>V_DD</td>
<td>7</td>
<td>7.8</td>
<td>8.4</td>
<td>V_DC</td>
</tr>
<tr>
<td>Voltage Regulator Output</td>
<td>V_IN=15V to 550V, I_DD=0, I_GATE=0</td>
<td>V_DD</td>
<td>7</td>
<td>7.8</td>
<td>8.4</td>
<td>V_DC</td>
</tr>
<tr>
<td>Voltage Regulator Output</td>
<td></td>
<td>I_DD</td>
<td>-</td>
<td>2</td>
<td></td>
<td>mA</td>
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<tr>
<td>Voltage Regulator Output</td>
<td></td>
<td>ΔV_DD</td>
<td>-</td>
<td>150</td>
<td>200</td>
<td>mV</td>
</tr>
<tr>
<td>PWM Dimming</td>
<td>PWM Input Low Voltage</td>
<td>V_PWMD(low)</td>
<td>-</td>
<td>-</td>
<td>0.5</td>
<td>V</td>
</tr>
<tr>
<td>PWM Dimming</td>
<td>PWM Input High Voltage</td>
<td>V_PWMD(high)</td>
<td>2.4</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>PWM Dimming</td>
<td>PWMD Pull-Down Resistance</td>
<td>R_PWMD</td>
<td>70</td>
<td>115</td>
<td>165</td>
<td>kΩ</td>
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<tr>
<td><strong>Current Sense Comparator</strong></td>
<td>Current Sense Input Current</td>
<td>I_IL</td>
<td>-5</td>
<td>-</td>
<td>5</td>
<td>μA</td>
</tr>
<tr>
<td>Current Sense Input Current</td>
<td>Current Sense Input Current</td>
<td>I_IH</td>
<td>-5</td>
<td>0</td>
<td>5</td>
<td>μA</td>
</tr>
<tr>
<td>Voltage Regulator Output</td>
<td>Current Sense Threshold Voltage</td>
<td>V_CS(high)</td>
<td>200</td>
<td>250</td>
<td>300</td>
<td>mV</td>
</tr>
<tr>
<td>Current Sense Blankening Interval</td>
<td></td>
<td>t_BLANK</td>
<td>-</td>
<td>400</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>Delay from CS Trip to Gate Low</td>
<td></td>
<td>t_DELAY</td>
<td>-</td>
<td>300</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>Minimum Off-Time One-Shot</td>
<td>Minimum Off-Time</td>
<td>t_off</td>
<td>6</td>
<td>-</td>
<td>8</td>
<td>μs</td>
</tr>
<tr>
<td><strong>Gate Driver</strong></td>
<td>Gate High Output Voltage</td>
<td>I_OUT=+10mA</td>
<td>V_GATE(high)</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Gate Low Output Voltage</td>
<td>I_OUT=-10mA</td>
<td>V_GATE(low)</td>
<td>-0.03</td>
<td>0.3</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Gate Output Rise Time</td>
<td>C_GATE=500pF</td>
<td>t_RISE</td>
<td>-</td>
<td>16</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>Gate Output Fall Time</td>
<td>C_GATE=500pF</td>
<td>t_FALL</td>
<td>-</td>
<td>7</td>
<td>-</td>
<td>ns</td>
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</tbody>
</table>

1.6 Thermal Characteristics

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Package</th>
<th>Symbol</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal Resistance,</td>
<td>SOIC-8 With Thermal Pad (NE)</td>
<td>R_{NIJA}</td>
<td>-</td>
<td>50</td>
<td>-</td>
<td>°C/W</td>
</tr>
<tr>
<td>Junction-to-Ambient</td>
<td>SOIC-8 W/O Thermal Pad (N)</td>
<td>-128</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

1 Use of a four-layer PCB can improve thermal dissipation (reference EIA/JEDEC JESD51-5).
2. Functional Description

Figure 1 Typical Application Circuit

2.1 Overview

The CPC9909 drives the LEDs via a minimum off-time, peak-current-limited, pulse-frequency modulation scheme. This control scheme is inherently stable, and the driver can be operated above a 50% duty cycle without any open-loop instability or sub-harmonic oscillations. Since the switching frequency depends on the LED load current, it results in a high efficiency operation.

2.2 LED Driver Theory of Operation

The typical application circuit is as shown in Figure 1. When PWMD is high, the control circuit is enabled and the gate driver turns on the external power MOSFET (Q1), causing the inductor (L1) current to ramp up until the voltage across the current sense resistor (RSENSE) exceeds VCS(high). When the voltage at the CS pin exceeds this threshold, the gate driver turns Q1 off. Q1 remains off for the duration of the fixed minimum off-time. While the switch is off, the inductor continues to deliver the current to the load through the diode (D1). When the off-time expires, Q1 turns on again until the peak current limit is reached, and the process repeats.

The peak current limit threshold is set by the external sense resistor, RSENSE, and the internal voltage threshold, VCS(high). This internal voltage threshold can also be set externally via the LD pin. The lower of these two thresholds and RSENSE set the peak current in the inductor.

A soft start function can be implemented by ramping up the DC voltage at the LD pin from 0V to VCS(high) at the desired rate. To utilize the soft start function, connect a resistor divider from VDD to ground and a capacitor from the LD pin to ground, as shown in Figure 2 Soft-Start Circuit.
2.3 Input Voltage Regulator

The CPC9909 has an internal voltage regulator that can work with input voltages ranging from 12VDC to 550VDC. When a DC voltage greater than 12V is applied at the \( V_{IN} \) pin, the internal voltage regulator regulates the voltage down to a typical 7.8V. The \( V_{DD} \) pin is the internal voltage regulator output pin and must be bypassed by a low-ESR capacitor to provide a low impedance path for high frequency switching noise.

The CPC9909 driver does not require the bulky start-up resistors typically needed for off-line controllers. The internal voltage regulator provides sufficient voltage and current to power internal IC circuits. This voltage is also available at the \( V_{DD} \) pin, and can be used as a bias voltage for external circuitry.

The internal voltage regulator can be bypassed by applying an external DC voltage to the \( V_{DD} \) pin that is slightly higher than the internally generated regulator voltage. This reduces the power dissipation of the integrated circuit, and it is more suitable in isolated applications where an auxiliary winding can be used to drive the \( V_{DD} \) pin.

The total input current drawn from the \( V_{IN} \) pin is equal to the quiescent current drawn by the internal circuitry (which is specified at 0.6mA maximum) plus the gate driver current. See “Shut-Down Mode Supply Current” in Section 1.5 “Electrical Characteristics” on page 5.

The current draw of the gate driver depends on the switching frequency and the gate charge of the external power MOSFET. The total input current can be calculated by:

\[
I_{IN} \approx 0.6mA + (Q_{GATE} \times f_S)
\]

Where \( Q_{GATE} \) is the total gate charge of the MOSFET and \( f_S \) is the oscillator external frequency.

2.4 Current Sense Resistor

The peak LED current is set by an external sense resistor \( (R_{SENSE}) \) connected from the CS pin to ground.

The value of the current sense resistor is calculated based on the average LED current desired, the current sense threshold, and the inductor ripple current.

\[
R_{SENSE} = \frac{V_{CS(\text{high})}}{I_{LED} + 0.5\Delta I_L}
\]

Where:
- \( V_{CS(\text{high})} \) = current sense threshold \( = 0.25V \) (or \( V_{LD} \))
- \( I_{LED} \) = average LED/inductor current
- \( \Delta I_L \) = inductor ripple current \( = 0.3*I_{LED} \)

Combining terms:

\[
R_{SENSE} = \frac{V_{CS(\text{high})}}{1.15 \times I_{LED}}
\]

2.5 Current Sense Blanking

The CPC9909 has an internal current sense blanking circuit. When the power MOSFET is turned on, the external inductor can cause an undesired spike at the current sense pin, initiating a premature termination of the gate pulse. To avoid this condition, a typical 400ns internal leading edge blanking time is implemented, thereby eliminating the need for external RC filtering, and simplifying the design. During the current sense blanking time, the current limit comparator is disabled, preventing the gate-drive circuit from terminating the gate-drive signal.

2.6 Enable/Disable Function

Connecting the PWMD pin to \( V_{DD} \) enables the gate driver. Connecting PWMD to \( GND \) disables the gate driver and sets the device in standby mode. In standby mode, the quiescent current is 0.6mA maximum.

2.7 Minimum Off-Time One-Shot

The CPC9909 uses a fixed off-time control scheme. The minimum off-time is set by an external resistor connected between the RT and GND terminals.

The off-time can be determined by:

\[
t_{off(\mu s)} = \left\{ \left[ \frac{R_A(\mu\Omega)}{66} \right] + 0.8 \right\}
\]

Off-time selection indirectly determines the switching frequency of the LED driver.
The switching frequency is determined by:

\[ F_S = \frac{1 - D}{t_{off}} \]

Where:
- \( D \) = duty cycle
- \( t_{off} \) = Off-time

In general, switching frequency selection is based on the inductor size, controller power dissipation, and the input filter capacitor.

The typical off-line LED driver switching frequency, \( f_S \), is between 30 kHz and 120 kHz.

This operating range gives the designer a reasonable compromise between switching losses and inductor size. The internal off-time one-shot has an accuracy of \( \pm 20\% \). The figure below shows the \( R_T \) resistor selection for the desired off-time.

### 2.8 Inductor Design

The inductor value is defined by the LED/inductor ripple current, minimum off time, and the output voltage. The minimum off time is determined by the duty cycle and switching frequency. The duty cycle is given by:

\[ D = \frac{V_{\text{LED string}}}{V_{\text{in(min)}}} \]

Where:
- \( V_{\text{LED string}} \) is the LED string voltage at the desired average LED current.
- \( V_{\text{in(min)}} \) is the minimum DC input voltage.

The minimum inductor value for a given ripple current is:

\[ L_{\text{MIN}} = \left[ \frac{V_{\text{LED string}}}{\Delta I_L} \right] \times t_{off} \]

Where:
- \( \Delta I_L \) = Ripple Current

The inductor peak current is given by:

\[ I_{\text{Peak}} = I_{\text{LED}} + 0.5 \Delta I_L \]

### 2.9 Gate Output Drive

The CPC9909 uses an internal gate drive circuit to turn on and off an external power MOSFET. The gate driver can drive a variety of MOSFETs. For a typical off-line application, the total MOSFET gate charge will be less than 25nC.
2.10 Linear Dimming

A linear dimming function can be implemented by applying a DC control voltage to the LD pin. By varying this voltage from 0V to \( V_{CS\text{(high)}} \), the user can adjust the current level in the LEDs which in turn will increase or decrease the light intensity. The control voltage to the LD pin can be generated from an external voltage divider network from \( V_{DD} \). This function is useful if the user requires LED current of a particular level, and there is no exact \( R_T \) value available. Note that applying a voltage higher than the current sense threshold voltage to the LD pin will not change the output current due to the fixed internal threshold setting. When the LD pin is not used, it should be connected to \( V_{DD} \).

Figure 3 Typical Linear Dimming Application Circuit

2.11 PWM Dimming

Pulse width modulation dimming can be implemented by driving the PWMD pin with a low frequency square wave signal in the range of a few hundred Hertz. The PWMD signal controls the LED brightness by gating the PWM gate driver output pin GATE. The signal can be generated by a microcontroller or a pulse generator with a duty cycle proportional to the amount of desired light output.

Figure 4 Buck Driver for PWM Dimming Application Circuit

2.12 Combination Linear and PWM Dimming

A combination of linear and PWM dimming techniques can be used to achieve a large dimming ratio.
3. Manufacturing Information

3.1 Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingestion. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) rating as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

<table>
<thead>
<tr>
<th>Device</th>
<th>Moisture Sensitivity Level (MSL) Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPC9909N / CPC9909NE</td>
<td>MSL 1</td>
</tr>
</tbody>
</table>

3.2 ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

3.3 Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of J-STD-020 must be observed.

<table>
<thead>
<tr>
<th>Device</th>
<th>Maximum Temperature x Time</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPC9909N / CPC9909NE</td>
<td>260°C for 30 seconds</td>
</tr>
</tbody>
</table>

3.4 Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable, and the use of a short drying bake may be necessary. Chlorine-based or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.
3.5 Mechanical Dimensions

3.5.1 CPC9909N Package

Note: Thermal pad should be electrically connected to GND, pin 3.

3.5.2 CPC9909NE Package

NOTES:
1. Complies with JEDEC Standard MS-012.
2. All dimensions are in millimeters.
3. Dimensions do not include mold flash or burrs
3.5.3 CPC9909NTR & CPC9909NETR Tape & Reel Specification

**Note:** Tape dimensions not shown comply with JEDEC Standard EIA-481-2

For additional information please visit [www.ixysic.com](http://www.ixysic.com)

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